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4807 Spicewood Springs Road Building 4, Suite 201 Austin, Texas 78759 T: 512-439-5080 F: 512-439-5099

FACSIMILE COVER SHEET

То:	Office of Initial Patent Examination's Filing Receipt Corrections	From:	D'Ann Naylor Rifai / vs		
Fax:	703.746.9195	Date:	March 5, 2004		
Phone:		Pages:	9 (including cover sheet)		
Re:	US Serial No.: 10/724,245; Filed: 11/28/03	Atty Docket No.:	VRT0107US		

Message:

Attached is a Request for Corrected Filing Receipt and supporting documents in regards to the above-referenced patent application.

If you do not receive all pages, please call (512) 439-5080.

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Chirag Deepak Dalal, Vaijayanti Rakshit Bharadwaj, Vivek V.

Gupta

Assignee:

VERITAS Operating Corporation

Title:

Identification For Reservation Of Replacement Storage Devices

For A Logical Volume To Satisfy Its Intent

Serial No.:

10/724,245

Filing Date:

November 28, 2003

Examiner:

Unknown

Group Art Unit:

2188

Docket No.:

VRT0107US

Austin, Texas March 5, 2004

Office of Initial Patent Examination's
Filing Receipt Corrections
COMMISSIONER FOR PATENTS
P. O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Dear Sir:

Applicants hereby request the Commissioner for Patents to correct the Official Filing Receipt for the above-identified patent application as follows:

	1.	Correct the Filing Date
\boxtimes	2.	Correct Inventor Name
	3.	Correct Title
	4.	Correct information referencing related case
	5.	Correct Attorney Docket Number
	6.	Other:

This Request is submitted for the following reason:

Under Applicant(s), please correct the third named inventor's name to read:

"Vivek V. Gupta"

Attached is a copy of the Filing Receipt having the above errors and the changes noted thereon, as well as copies of the Declaration and the first page of the Application as filed. Therefore, it is respectfully requested that a corrected Filing Receipt be issued.

Because this was not an error on the part of Applicants, Applicants believe no fee is required. However, the Commissioner is hereby authorized to charge any fee that may be required for this correction to Deposit Account No. 502306.

If this action does not lead to issuance of a corrected Official Filing Receipt as requested, please contact the undersigned.

Certificate of Facsimile Transmission

I hereby certify that this correspondence is being transmitted via facsimile to the Office of Initial Patent Examination's Filing Receipt Corrections, fax number 703-746-9195 on March 5, 2004.

Attorney for Applicant(s)

Date of Signature

Respectfully submitted,

D'Ann Naylor Rifai

Attorney for Applicant(s)

Reg. No. 47,026 512-439-5086

512-439-5099 (fax)



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CONFIRMATION NO. 1642

D'Ann Naylor Rifai CAMPBELL STEPHENSON ASCOLESE LLP Building 4, Suite 201 4807 Spicewood Springs Road Austin, TX 78759

FILING RECEIPT "OC000000011997372"

Date Mailed: 03/01/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Chirag Deepak Dalal, Mumbai, INDIA; Vaijayanti Rakshit Bharadwaj, Sunnyvale, CA; Vivek V. Gupta, Pune, INDIA; Vivek

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 03/01/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Identification for reservation of replacement storage devices for a logical yolume to satisfy its intent

F-559

Preliminary Class

711



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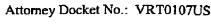
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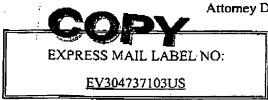
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F-559





IDENTIFICATION FOR RESERVATION OF REPLACEMENT STORAGE DEVICES FOR A LOGICAL VOLUME TO SATISFY ITS INTENT

Chirag Deepak Dalal Vaijayanti Rakshit Bharadwaj Vivek Gupta

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BACKGROUND OF THE INVENTION

[0002] As businesses increasingly rely on computers for their daily operations, managing the vast amount of business information generated and processed has become a significant challenge. Most large businesses have a wide variety of application programs managing large volumes of data stored on many different types of storage devices across various types of networks and operating system platforms. These storage devices can include tapes, disks, optical disks, and other types of storage devices and often include a variety of products produced by many different vendors. Each product typically is incompatible with the products of other vendors.

[0003] Historically, in storage environments, physical interfaces from host computer systems to storage consisted of parallel Small Computer Systems Interface (SCSI) channels supporting a small number of SCSI devices. Whether a host could access a particular storage device depended upon whether a physical connection from the host to the SCSI device existed. Allocating storage for a particular application program was relatively simple.

[0004] Today, storage area networks (SANs) including hundreds of storage devices can be used to provide storage for hosts. SAN is a term that has been adopted by the storage industry to refer to a network of multiple servers and connected storage devices. A SAN can be supported by an underlying fibre channel network using fibre channel protocol and fibre channel switches making up a SAN fabric. Alternatively, a SAN can be supported by other